IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S):

REYNES, JEAN-MICHEL

GROUP ART UNIT:

2814

APPLN. NO.:

10/518158

EXAMINER:

John C. Ingham

FILED:

12/10/2004

TITLE:

POWER SEMICONDUCTOR DEVICE WITH A BASE REGION AND METHOD OF

MANUFACTURING SAME

Certificate of Submission

I hereby certify that this correspondence is being submitted to the U.S.P.T.O., Alexandria, VA.

- ☐ Addressed per C.F.R.§ 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.
- $\hfill \Box$ Facsimile transmitted in accordance with C.F.R.§1.6(d).
- $\ \, \boxtimes \,$ Submitted electronically via EFS in accordance with "Legal Framework for EFS Web".

5/24/2007

Date of Submission

<u>/Stacie Herrera/</u> Signature

0181111111

Stacie Herrera Printed Name of Person Signing Certificate

NOTICE OF APPEAL FROM THE EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SIR:

In accordance with 37 CFR 1.191, applicant hereby appeals to the Board of Patent Appeals and Interferences from the last decision of the examiner. The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 503079, Freescale Semiconductor, Inc.

Respectfully submitted,

RÉYNES, JEAN-MICHEL

FREESCALE SEMICONDUCTOR, INC.

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